



XA-10077

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hirotsugu KOJIMA et al.

Appln. No.: 10/823,581

Group Art Unit: 2138

Filed: April 14, 2004

Examiner: J. Kerveros

For: SEMICONDUCTOR INTEGRATED CIRCUIT AND ELECTRONIC SYSTEM

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AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In response to the Office Action dated April 17, 2006,
please amend this application as indicated below.

Amendments to the Claims are reflected in the listing
of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 10 of this paper.